

FIG. 7a

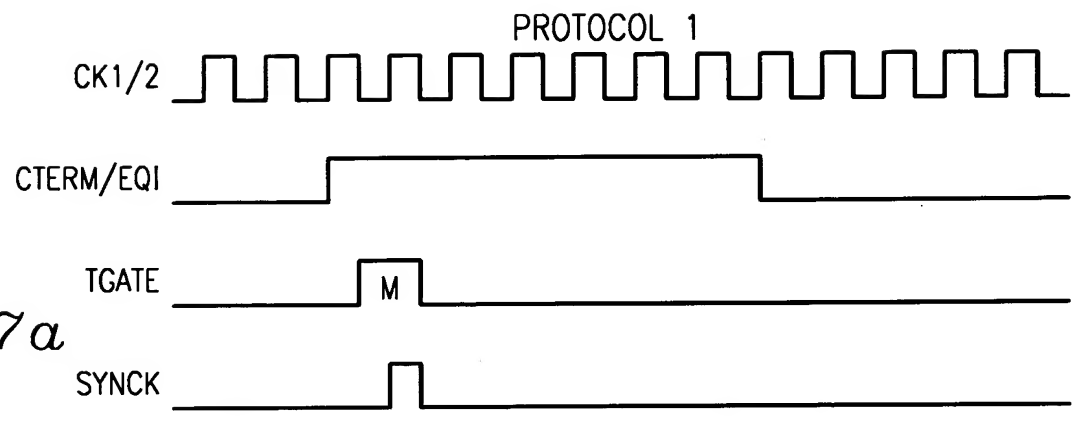


FIG. 7b

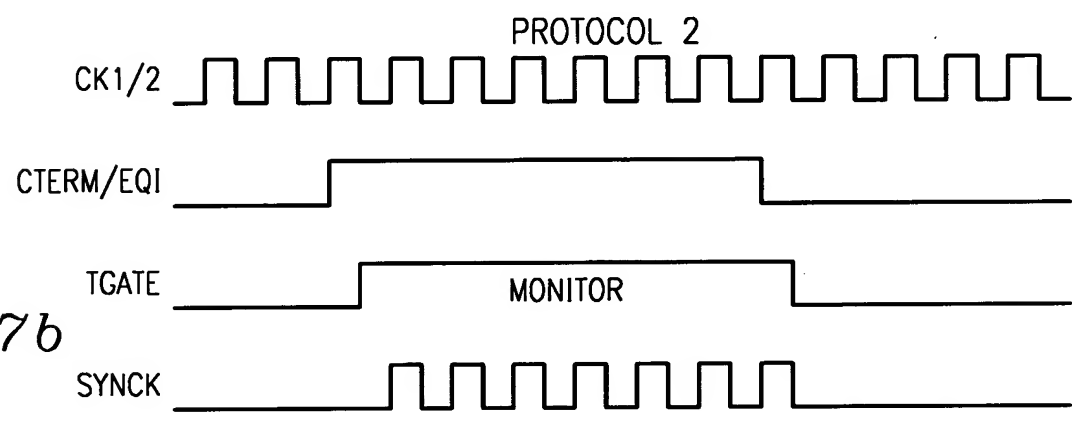


FIG. 7c

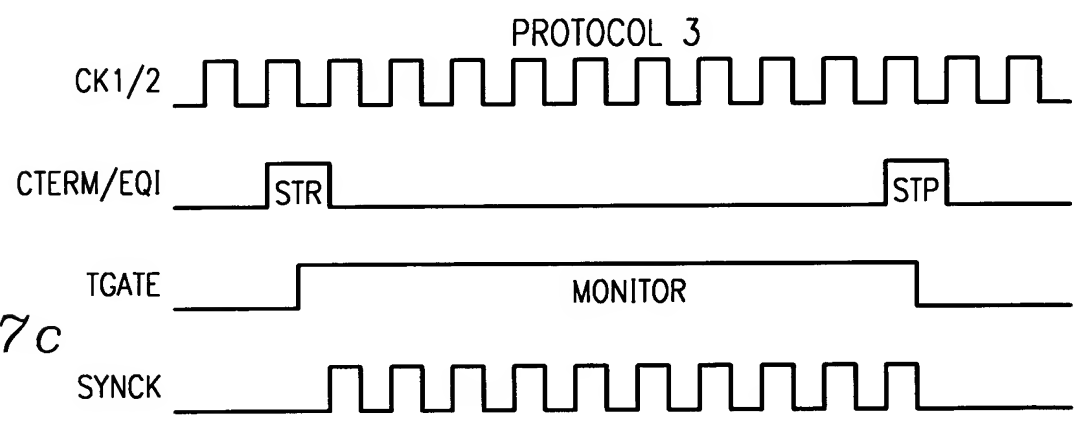
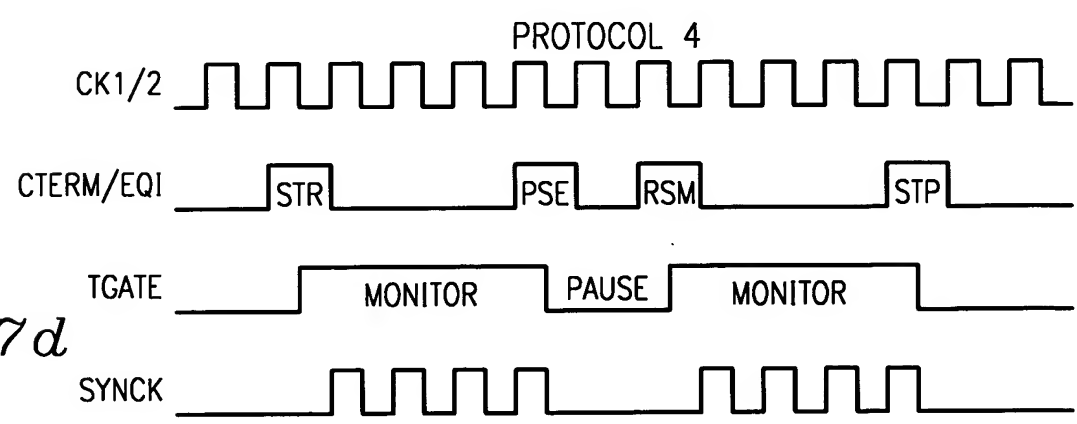
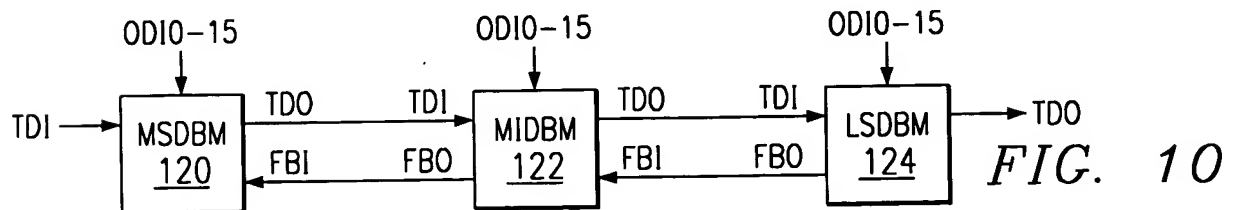
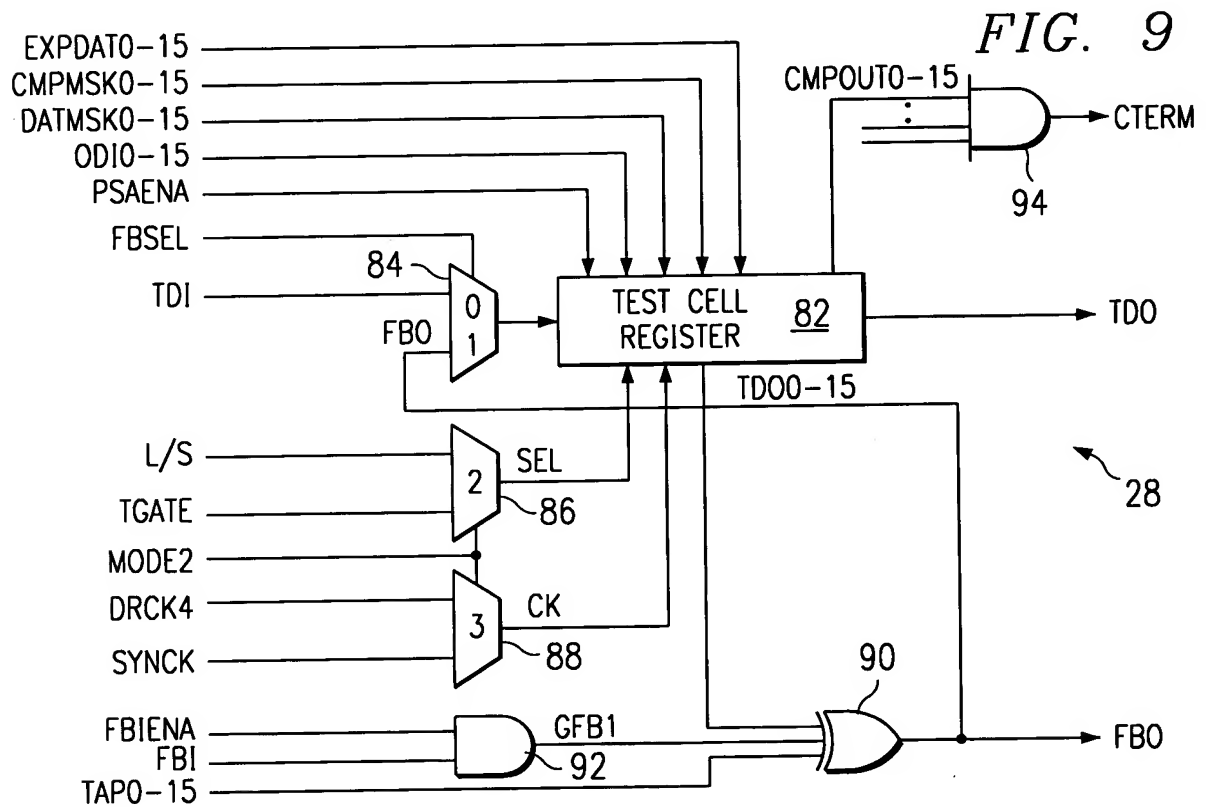
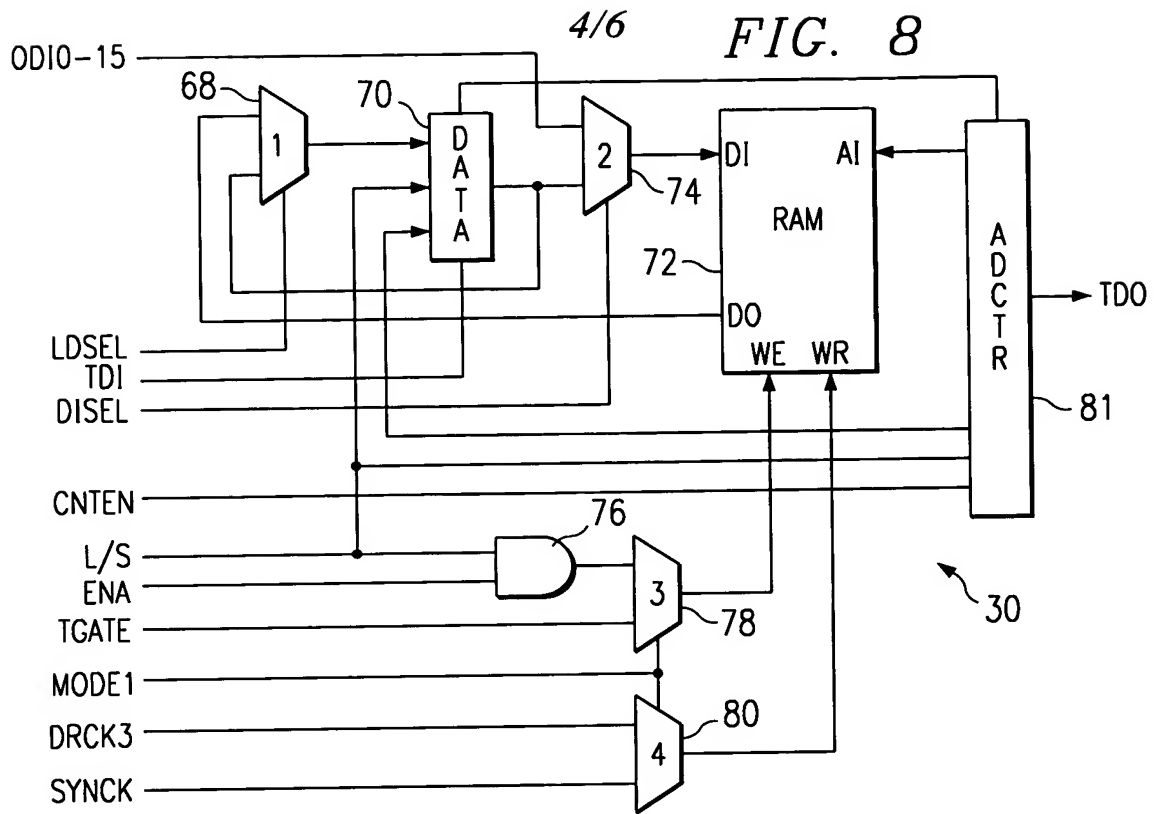


FIG. 7d





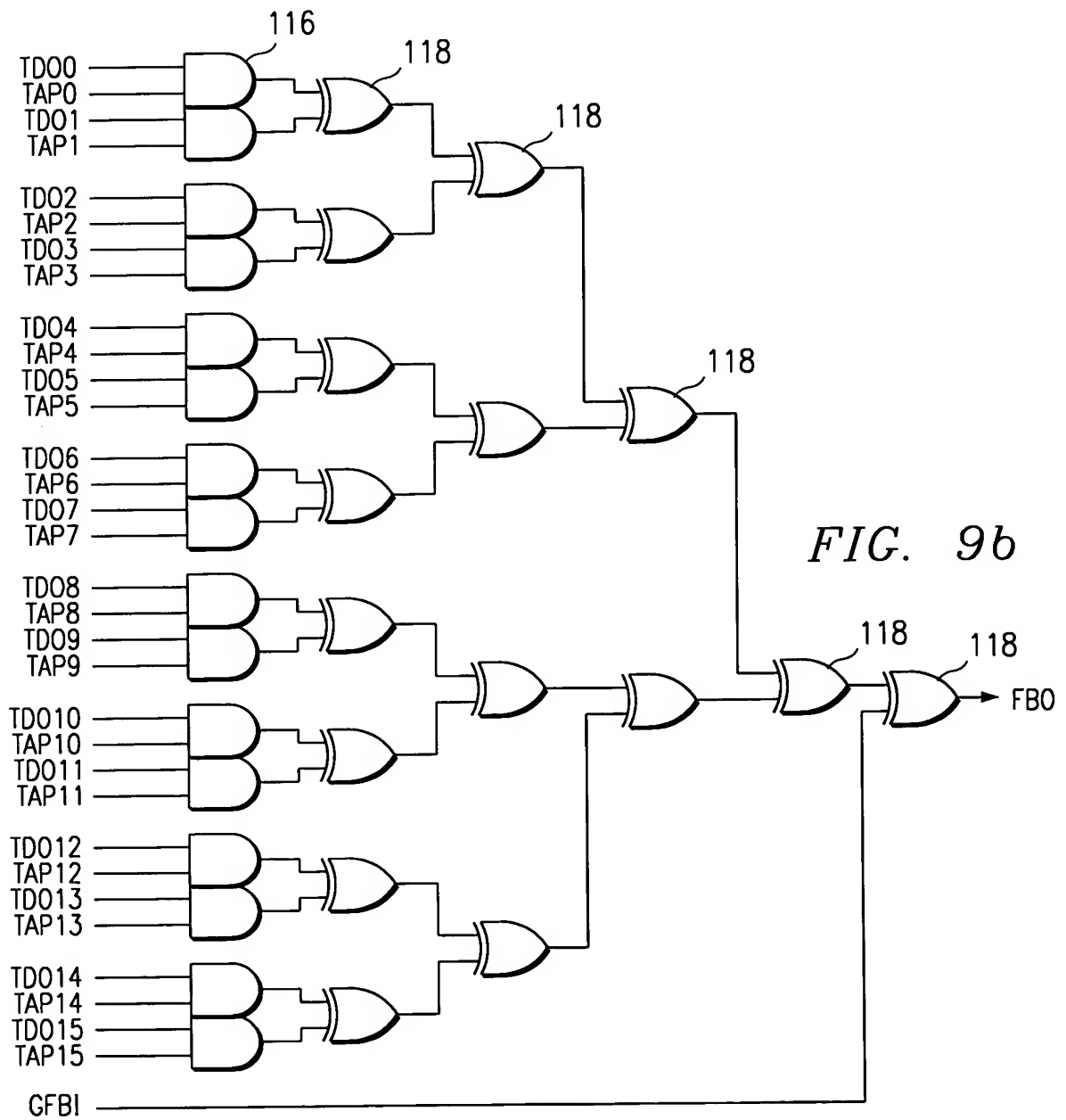
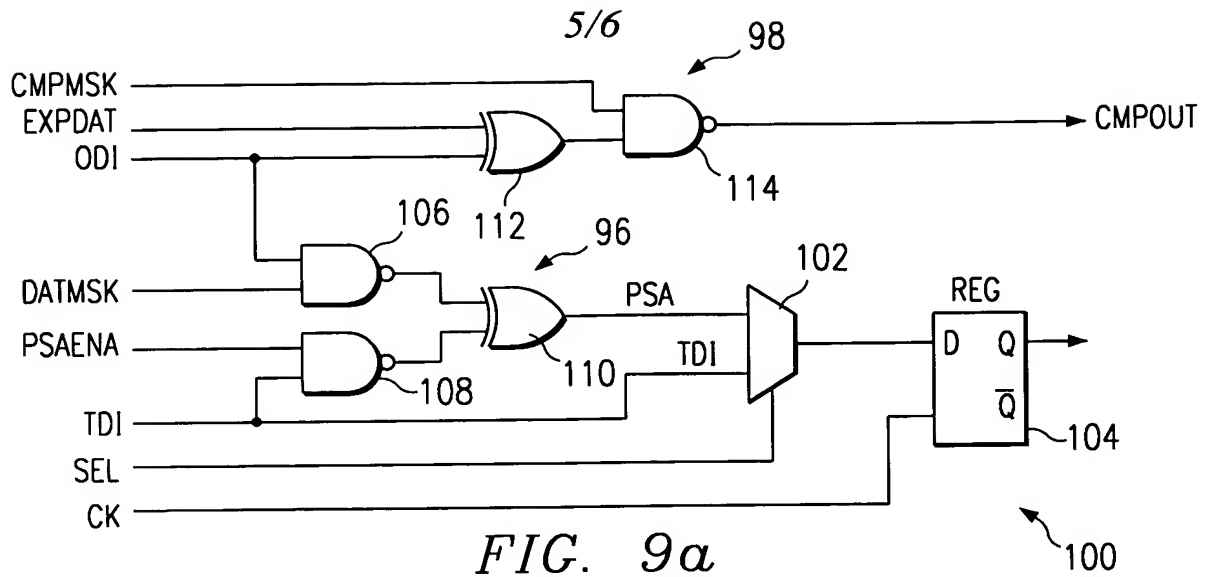
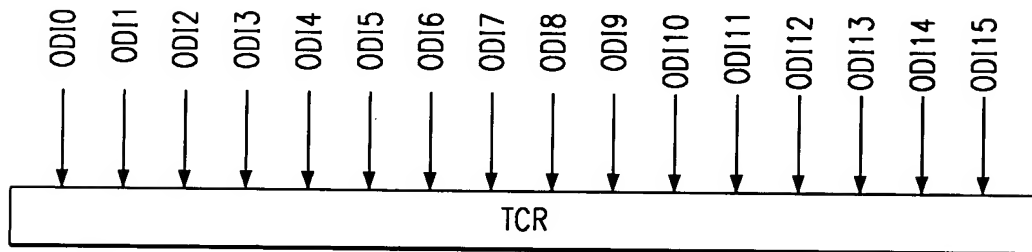


FIG. 9c

PERFORMING PARALLEL SIGNATURE ANALYSIS
ON ALL ODI INPUTS

*FIG. 9d*

PERFORMING SINGLE SIGNATURE ANALYSIS
ON ONE ODI INPUT

